



Attorney Docket no: 0553-0401

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yoshifumi Tanada

Serial No.: 10/807,692

Filed: March 24, 2004

For: Circuit For Inspecting Semiconductor Device
And Inspecting Method

Examiner: Vibol Tan

Art Unit: 2819

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Date: September 12 2006

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. In accordance with 37 CFR 1.98(a), a copy of US patents and publications is not enclosed. A copy of any other reference is enclosed herewith.

No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

As a RCE is being filed herewith, no fee is believed due for this IDS. If a fee should be due, please charge our Deposit Account No. 50/1039.

Respectfully submitted,



Mark J. Murphy

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(Use as many sheets as necessary)

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Application Number	10/807,692
Filing Date	March 24, 2004
First Named Inventor	Yoshifumi Tanada
An Unit	2819
Examiner Name	Vibol Tan
Attorney Docket Number	0553-0401

[illegible][illegible]Date
Considered

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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